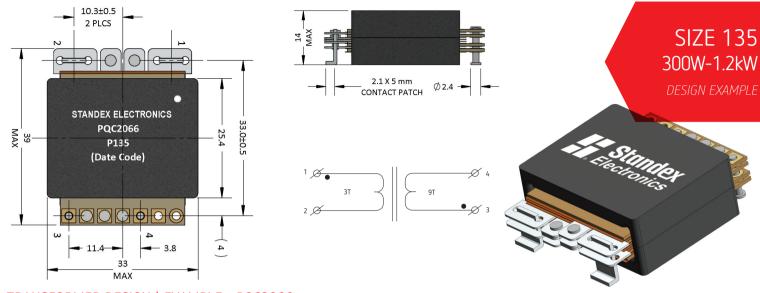
SOLUTIONS | Planar Transformers & Inductors



TRANSFORMER DESIGN | EXAMPLE - PQC2066

n	Topology	Full Bridge ZVS
2	Input Voltage	42-56VDC
3	Output Power (Output Voltage/Current After Rectification)	120VDC/3.5A (420W)
SPECIFICAL	Turns Ratio - Np/Nboost/Ns	3T/9T
7	Switching Frequency	200kHz
¥	Duty Cycle, Max. At Low Input Voltage	97.0%
IRICAL	Efficiency At Full Power Calculated	98.95% (4.4W losses)
ו ו	External Ambient Temp, Max.	+35°C

Temp. Rise, Hotspot Ambient, Max.	+58°C
Minimum Isolation Voltage	
Primary To Secondary	2121VDC
Secondary To Core	500VDC
Primary Inductance, Np, Min.	27μΗ
Primary Resistance, Np, Max.	1.8m0hm
Secondary Resistance, Ns, Max.	16m0hm
Leakage Inductance 1-2/3-4 Shorted, Typ.	50nH
Weight Range	50-150grams

NOTES:

1) FOR OPTIMAL PERFORMANCE A THERMALLY
CONDUCTIVE SUBSTRATE BETWEEN FERRITE AND
HEATSINK SHOULD BE UTILIZED

2) HEATSINK & THERMAL SOLUTIONS AVAILABLE